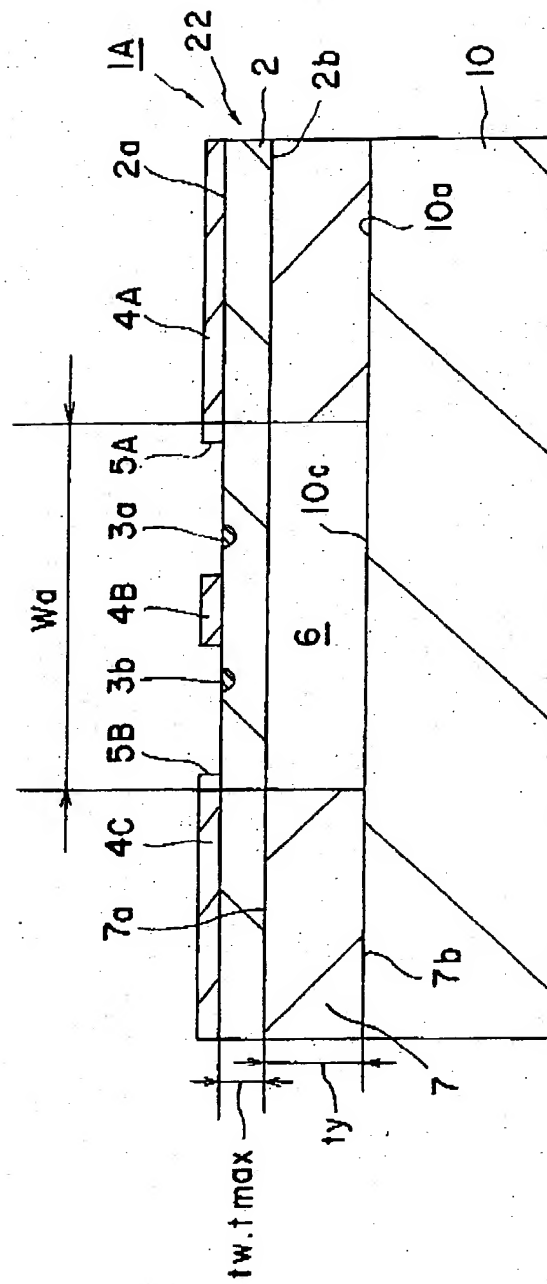


This diagram shows a cross-sectional view of a semiconductor device. It features a substrate with several layers and regions. At the top, a layer is labeled 1A. Below it is a layer labeled 2. A central region is labeled 3, with sub-regions 3a and 3b. Below layer 2, there are several layers labeled 4A, 4B, and 4C. A region labeled 5A is shown within a dashed box, and a region labeled 5B is shown below it. A dashed line labeled 6 is also present. On the right side, there are regions labeled 8A, 8B, 9A, and 9B. Directional arrows are labeled I, II, III, IV, and V, indicating different orientations or sections of the device.

Fig. 2



[illegible]



Fig. 5

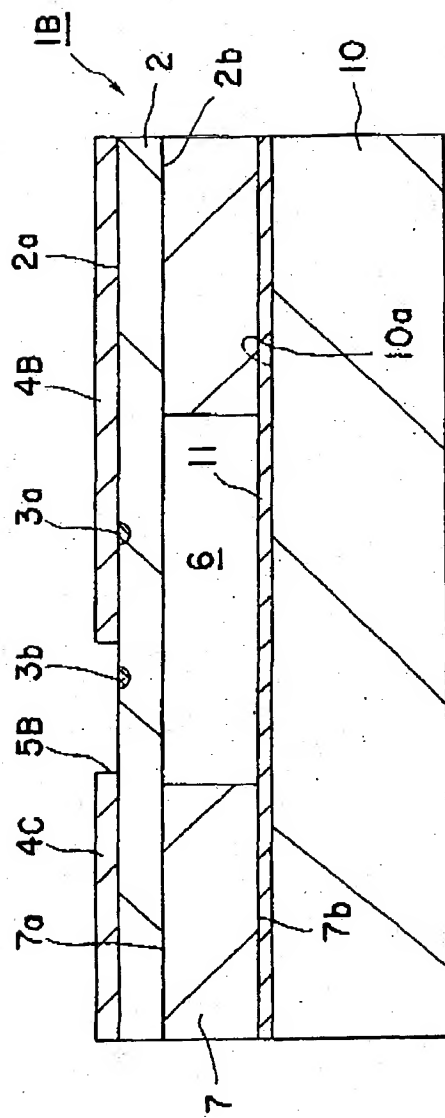


Fig. 6

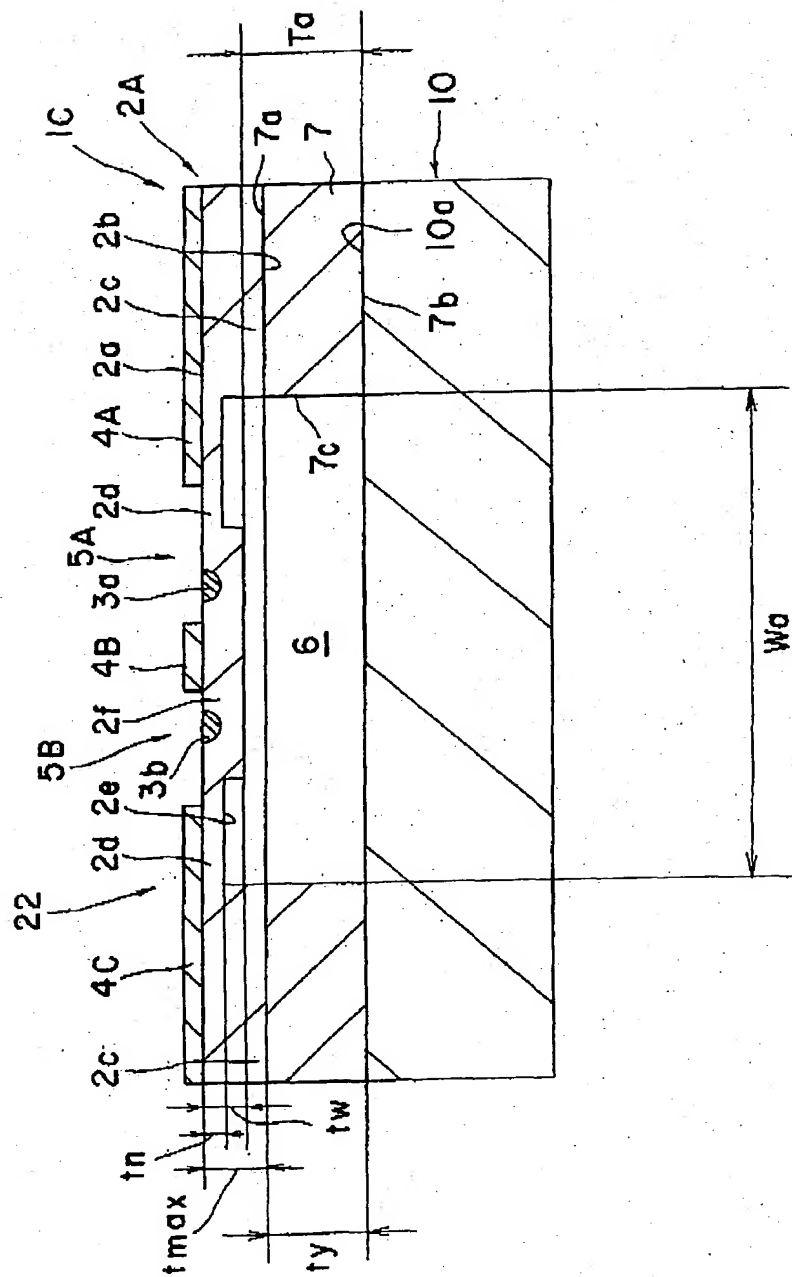








Fig. 9

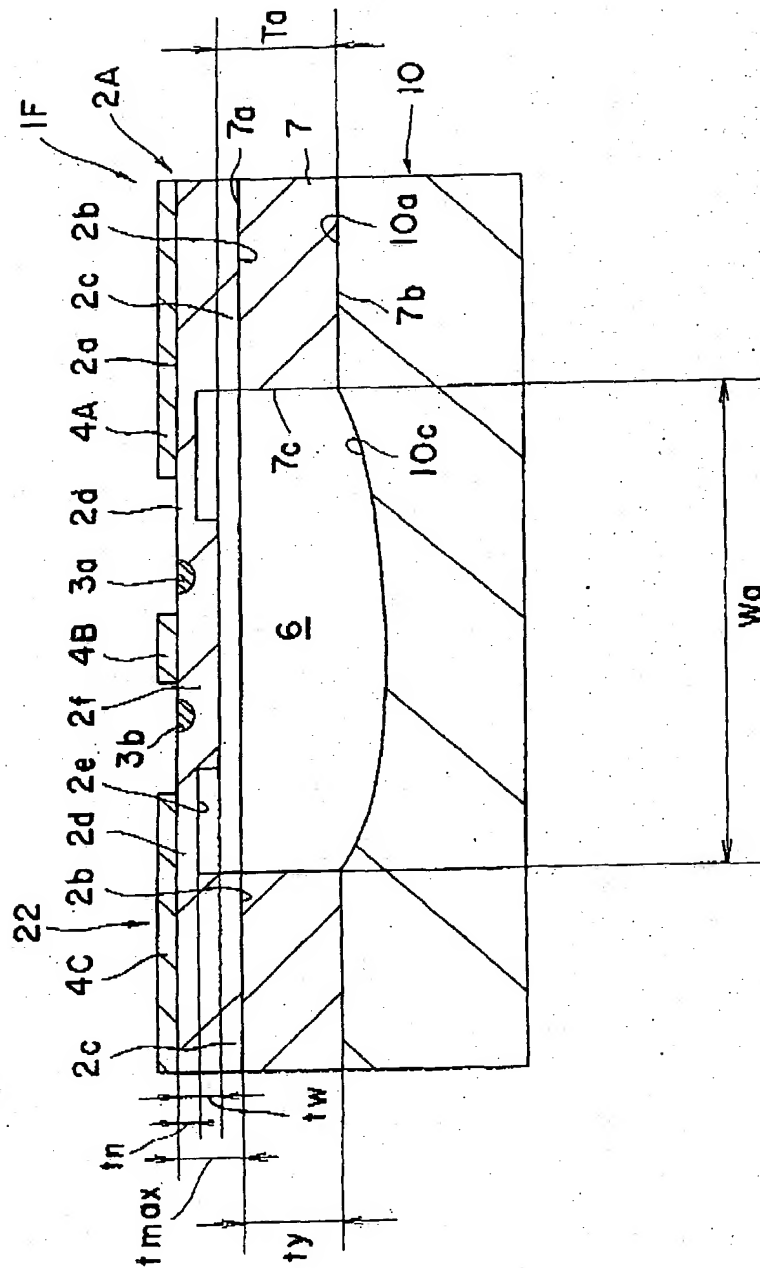


Fig. 10

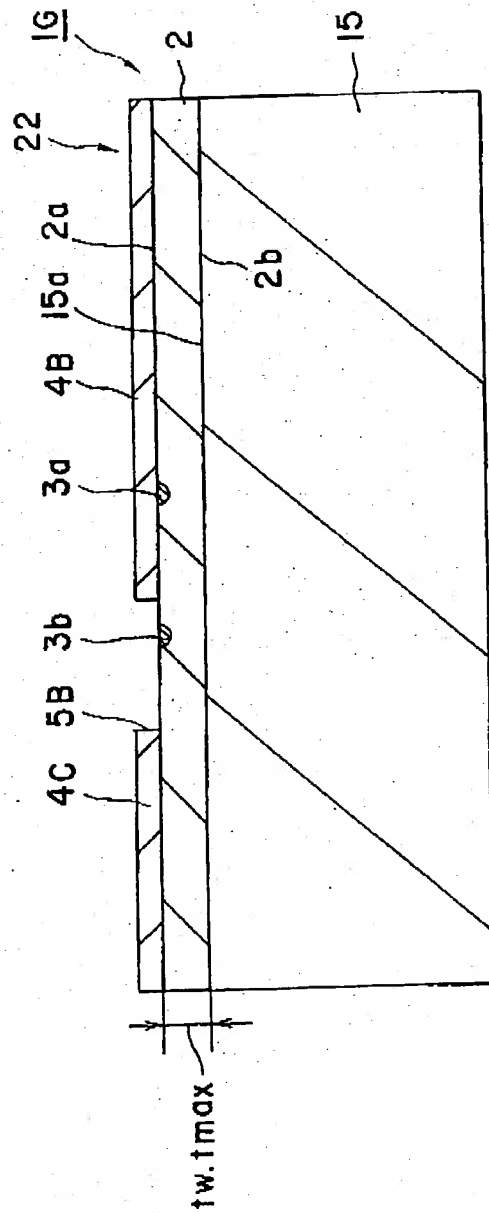


Fig. 11

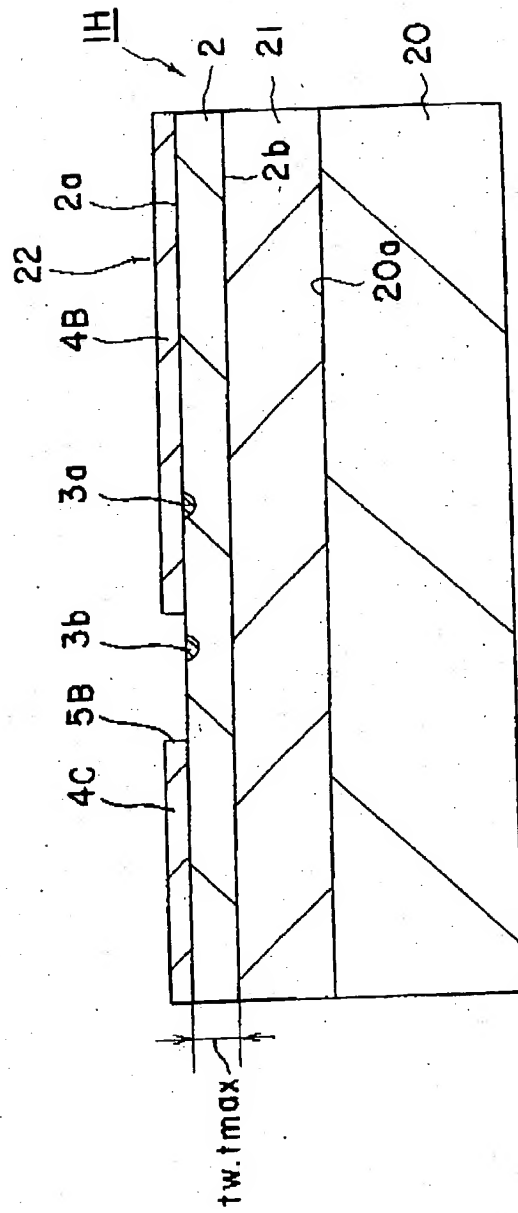


Fig. 12

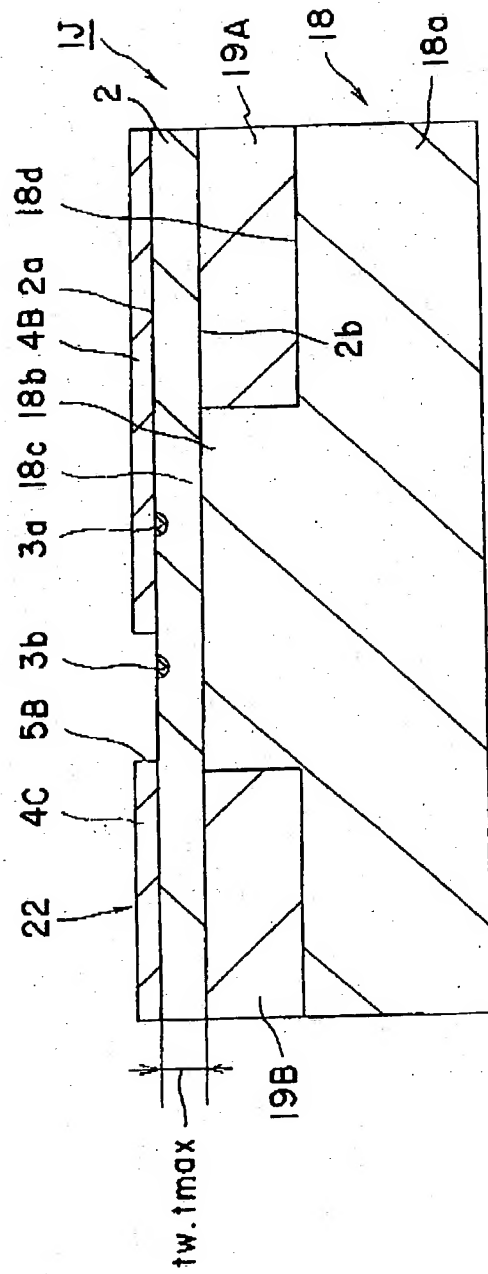


Fig. 13

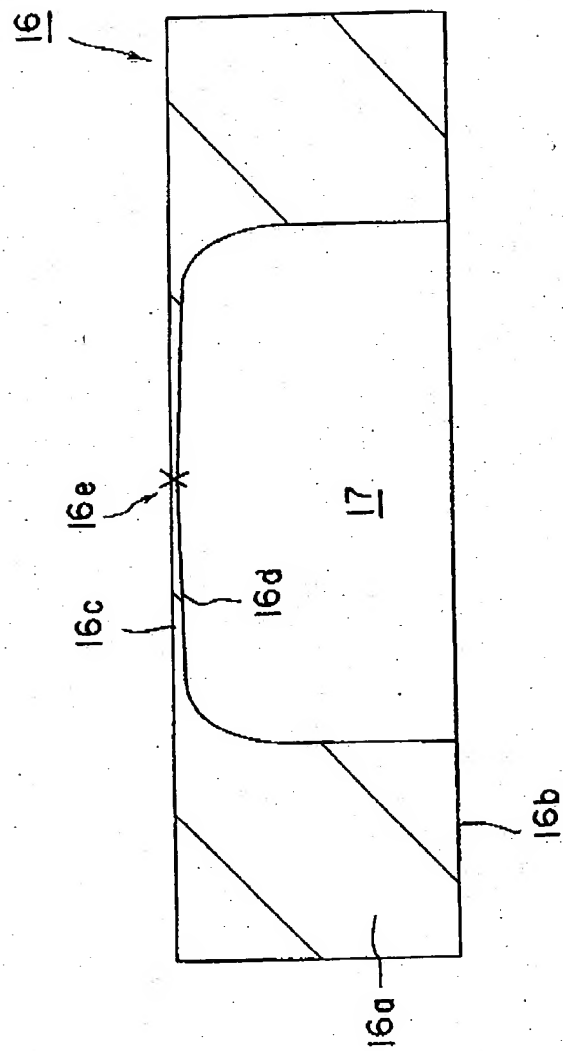


Fig. 14

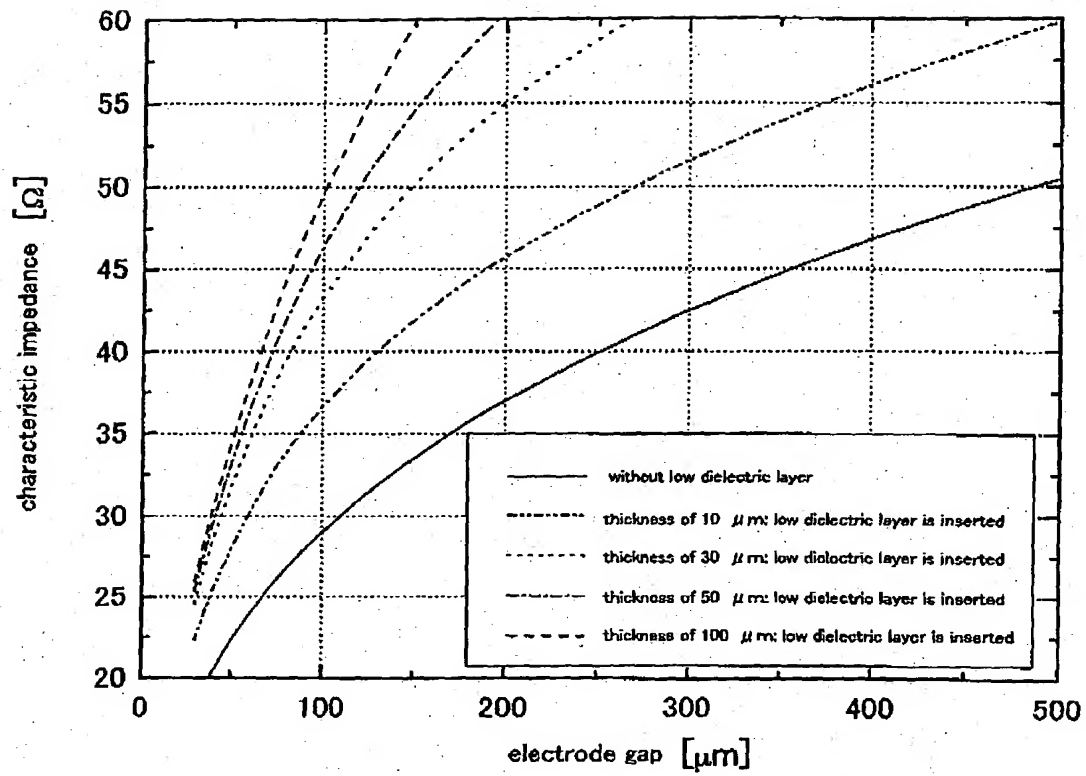


Fig. 15

